

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		09/960,495	AMISHIRO ET AL.	
		Examiner	Art Unit	Page 1 of 1
		David L. Hogans	2813	

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,121,643	09-2000	Gardner et al.	257/202
	B	US-2002/0084886	07-2002	Wu, Bing-Chang	338/307
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era (2000), Lattice Press, Second Edition, Volume 1, page 301
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.